

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,111,033 B2
APPLICATION NO. : 09/919495
DATED : September 19, 2006
INVENTOR(S) : Sebastien Ferroussat

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, line 51 should read:

of one sum bit and two carry bits. One of these carry bits is

Column 5, line 12 should read:

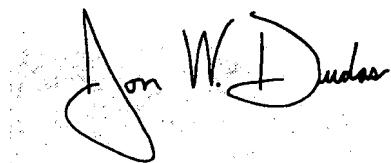
circumstances the value zero would be taken as the input to

line 53 should read:

Reference is now made to FIG. 6 which shows a 7 to 4

Signed and Sealed this

Seventeenth Day of July, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office